

embodiment. A data communication line for supplying compressed image information and a CD-ROM 34 as a database are connected to an image data generating apparatus 33. Further, a radio interface 32 for 5 transferring generated image data by a microwave is connected to the image data generating apparatus 33. Those elements are provided for the parent device 31 of the image display. Information is transferred and received wirelessly between the radio interface 32 in 10 the parent device 31 and a radio interface 2 in the child device 1. Four kinds of outputs are supplied from the radio interface 2. A first output is supplied to a moving image decoder 3 and to a write signal generating circuit 17 via a moving image write line 4. 15 A second output is supplied to a still image decoder 5, a still image memory 6, and to the write signal generating circuit 17 via a still image write line 7. A third output is supplied to a text code memory 8, an outline font generating circuit 9, and to the write 20 signal generating circuit 17 via a text write line 11. A fourth output is supplied to an icon/window address memory 12, an icon/window generating circuit 13, and to the write signal generating circuit 17 via an icon/window write line 15. An outline font ROM 10 and 25 an icon/window ROM 14 are connected to the outline

font generating circuit 9 and the icon/window generating circuit 13, respectively. An icon/window position detecting circuit 16 is also connected to the icon/window address memory 12. A timing generating circuit 20 is also provided and outputs of the position detecting circuit 16 and the timing generating circuit 20 control the still image memory 6, the text code memory 8, and the icon/window address memory 12 (the outputs are omitted here).

A display pixel array 18 for displaying an image by using a TN liquid crystal is further connected to the write signal generating circuit 17. The display pixel array 18 has a touch sensor and an output of the touch sensor is inputted to the radio interface 2 via a touch sensor output generating circuit 19.

Fig. 2 is a diagram showing the internal construction of the display pixel array 18. Display pixels are arranged in a matrix state in a display pixel area 53. Each pixel is constructed by a TN liquid crystal capacitor 49, a TFT switch 48 connected to the TN liquid crystal capacitor 49, and an AND gate circuit 47 for driving the gate of the TFT switch 48. The AND gate circuit 47 and the TFT switch 48 are formed by a CMOS process of a poly-Si TFT. The other terminal of the TFT switch 48 is connected to a signal

line 45 and input terminals of the AND gate circuit 47 are connected a vertical direction gate selection line 50 and a horizontal direction gate selection line 46 in the row and column directions, respectively. A 5 moving image signal output circuit 43 and a still image signal output circuit 41 are connected to the signal line 45. A moving image vertical direction selecting circuit 52 and a still image vertical direction selecting circuit 51 are connected to the 10 vertical direction gate selection line 50. A moving image horizontal direction selecting circuit 44 and a still image horizontal direction selecting circuit 42 are connected to the horizontal direction gate selecting line 46. The moving image signal output circuit 43, the still image signal output circuit 41, the moving image vertical direction selecting circuit 52, the still image vertical direction selecting circuit 51, the moving image horizontal direction selecting circuit 44, and the still image horizontal 15 direction selecting circuit 42 are connected to the write signal generating circuit 17. 20

The operation of the embodiment will be described hereinbelow with reference to Figs. 1, 2, and 3. As shown in Fig. 1, compressed image information inputted 25 from the data communication line and the CD-ROM 34 as